

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	Henry, G. Glenn Crispin, Thomas A. Elliott, Timothy A. Parks, Terry	Examiner:	
Docket:	CNTR.2230	Art Unit:	2131
Serial No.:	10/826632		
Filing Date:	4/16/04		
Title:	APPARATUS AND METHOD FOR GENERATING A CRYPTOGRAPHIC KEY SCHEDULE IN A MICROPROCESSOR		

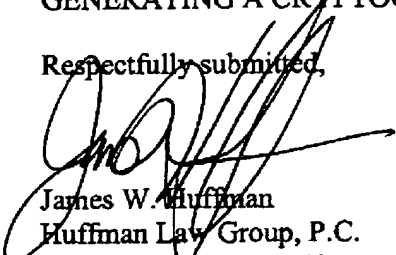
REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
Alexandria, VA 22313

Dear Sir:

The Filing Receipt dated 06/25/04 incorrectly indicates the Title to be "MICROPROCESSOR APPARATUS AND METHOD FOR PROVIDING CONFIGURABLE CRYPTOGRAPHIC KEY SIZE". Please correct the Title to be "APPARATUS AND METHOD FOR GENERATING A CRYPTOGRAPHIC KEY SCHEDULE IN A MICROPROCESSOR".

Respectfully submitted,


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Date: 6-30-04

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JCWSCS 02 JUL 2004



UNITED STATES PATENT AND TRADEMARK OFFICE

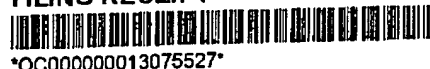
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	RL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/826,632	04/16/2004	2131	878	CNTR.2230	11	26	3

CONFIRMATION NO. 1329

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FILING RECEIPT



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Applicant(s)

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Assignment For Published Patent Application:

VIA Technologies Inc., Taipei, TAIWAN;

Domestic Priority data as claimed by applicant

This appln claims benefit of 60/506,971 09/29/2003
 and claims benefit of 60/507,001 09/29/2003
 and claims benefit of 60/506,978 09/29/2003
 and claims benefit of 60/507,004 09/29/2003
 and claims benefit of 60/507,002 09/29/2003
 and claims benefit of 60/506,991 09/29/2003
 and claims benefit of 60/507,003 09/29/2003
 and claims benefit of 60/464,394 04/18/2003
 and claims benefit of 60/506,979 09/29/2003
 and claims benefit of 60/508,927 10/03/2003
 and claims benefit of 60/508,679 10/03/2003
 and claims benefit of 60/508,076 10/02/2003
 and claims benefit of 60/508,604 10/03/2003
 and is a CIP of 10/674,057 09/29/2003
 and is a CIP of 10/800,983 03/15/2004

Foreign Applications

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If Required, Foreign Filing License Granted: 06/25/2004

Projected Publication Date: 10/21/2004

Non-Publication Request: No

Early Publication Request: No

Title APPARATUS AND METHOD FOR GENERATING A CRYPTOGRAPHIC
~~Microprocessor apparatus and method for providing configurable cryptographic key size~~
KEY SCHEDULE IN A MICROPROCESSOR

Preliminary Class

713

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Title 37, Code of Federal Regulations, 5.11 & 5.15

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